

<b>Notice of References Cited</b>		Application/Control No. 09/448,276	Applicant(s)/Patent Under Reexamination LEE ET AL.	
		Examiner Timothy L Rude	Art Unit 2871	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,963,290	10-1999	Murai et al.	349/191
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ternent et al, Single Stage Amplifiers on a CMOS Grade Silicon Substrate using a Polymer Interlayer Dielectric with Strained Silicon MOSFETs, University of Glasgow, Glasgow, UK
	V	CRC Handbook of Chemistry and Physics, 78th Edition, 1997, Chemical Rubber Publishing Company, 3 pages only, especially page 13-12.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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